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SJ Eggers, JS Emer, HM Leby, JL Lo, RL Stamm, DM ... - Micro, IEEE, 1997 - [ieeexplore.ieee.org](#)

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... Multi-threaded superscalars, in addition to heightening instruction ... another thread, thereby exploiting thread-level parallelism. To gauge SMT's potential for ...

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... replicated or resized to support SMT or a multi-threaded workload. ... Our SMT architecture shares a ... in two ways not possible with single-threaded processors: (1 ...

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... The most compelling reason for running parallel applications on an SMT processor is its ability to use thread-level parallelism and instruction-level ...

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A El-Moursy, DH Albonesi - High-Performance Computer Architecture, 2003. HPCA-9 2003. ..., 2003 - [ieeexplore.ieee.org](#)

... Figure 5. Statistics for single-threaded and multi-threaded policies for the ... 50% of the time in single-threaded mode ... usage of queue re-sources in SMT and the ...

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DM Tullsen, JL Lo, SJ Eggers, HM Levy - International Symposium on Architectural Support for ..., 2000 - [doi.ieeeecs.org](#)

... SMT-block + SMT-specRel ... Efficient synchronizatin for multi-threaded processors. ...

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... a detailed simulation of an SMT processor execut ... DSS workloads on simultaneous multi-threaded processors. On conventional (single-threaded) processors, a DBMS ...

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C Zilles, G Sohi - ACM SIGARCH Computer Architecture News, 2001 - [doi.ieeecomputersociety.org](#)

... execute as helper threads on a multi-threaded machine. ... the path taken by the main thread to identify ... context of a simultaneous multithreading (SMT) processor ...

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SE Raasch, SK Reinhardt - Proc. of the Workshop on Multithreaded Execution And ..., 1999 - [eecs.umich.edu](#)

... pronounced in Tullsen et al's study of SMT fetch policies [2 ... If a multi-threaded processor decides to favor a more resource-efficient thread over a user ...

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... Hence single-threaded programs cannot use resources from ... Consequently, we design the Parallel On-chip Simultaneous ... processors: superscalar, SMT, CMP, and POSM. ...

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S Kaxiras, G Narlikar, AD Berenbaum, Z Hu - Proceedings of the international conference on Compilers, ..., 2001 - [portal.acm.org](#)

... efficiently, performing as well as a non-multi-threaded DSP but ... For the single-threaded experiments, we run 6 ... In the experiments with the SMT and CMP cores ...

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... and FGMT represents a fine-grained multi-threaded superscalar ... waste, the fine-grained

multithreaded architecture ... than the fine-grained multithreaded processor ...

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ing of ... several concerns about simultaneous multithreaded processor design ...

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... processing on a simultaneous multithreading architecture. ... a fine-grained multithreaded

processor ... In contrast, simultaneous multithreading avoids resource ...

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... Fine-grain multithreaded processors context switch as often ... shows simultaneous

multithreading. ... both instruction-level parallelism and thread-level parallelism ...

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SK Reinhardt, SS Mukherjee - ACM SIGARCH Computer Architecture News, 2000 - [portal.acm.org](#)

... from a Simultaneous Multithreaded (SMT) processor—provides ... Simultaneous multithreading

(SMT) [18][19][21] is a ... 3] implements a four-threaded SMT machine that ...

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E Rotenberg - Fault-Tolerant Computing, 1999. Digest of Papers. Twenty- ..., 1999 - [ieeexplore.ieee.org](#)

... ture, in this case simultaneous multithreading, to achieve ... SMT model exploits

simultaneous multithreading to ... work on simultaneous multithreaded machines [10 ...

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[IBM Power5 chip: a dual-core multithreaded processor - group of 17 »](#)

R Kalla, B Sinharoy, JM Tendler - Micro, IEEE, 2004 - [ieeexplore.ieee.org](#)

... multithreading (SMT), as in other multithreaded implementations ... Although a higher

level of multithreading is possible ... a group come from the same thread and are ...

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... management systems on simultaneous multi-threaded processors. ... 8-wide simultaneous

multithreaded processor ... we demonstrate that simultaneous multithreading can ...

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... Efficient synchronization for multi-threaded processors. In Workshop on Multithreaded

Execution Architecture ... Simultaneous multithreading: Maximizing on-chip ...

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P Kongetira, K Aingaran, K Olukotun - IEEE Micro, 2005 - [doi.ieeecomputersociety.org](#)

... called time-sliced or vertical multithreaded. ... multithreading (SMT) or, alternately,

horizontal multithreading. ... instantiating single-threaded processor cores on ...

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V Krishnan, J Torrellas - Proceedings of the International Parallel Processing ..., 1998 - doi.ieeeecs.org

... SMT architecture when running parallel applications. ... the SMT4 processor has 4 clusters, each supporting 2 ... is much smaller than that of the centralized SMT. ...

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J Burns, JL Gaudiot - Parallel Architectures and Compilation Techniques, 2001. ..., 2001 - ieeexplore.ieee.org

... Consequently, we design the Parallel On-chip Simultaneous Multithreaded ... create two partitions within the SMT.pl.f2 ... extra stage delay of inter cluster forwarding ...

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SE Raasch, SK Reinhardt - Parallel Architectures and Compilation Techniques, 2003. ..., 2003 - ieeexplore.ieee.org

... insights to the design of SMT processors ... Allowing threads to share clusters—and thus execution ... 12th International Conference on Parallel Architectures and ...

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JL Lo, SJ Eggers, HM Levy, SS Parekh, DM Tullsen - International Journal of Parallel Programming, 1999 - Springer

... A true SMT workload would be multiprogrammed: for example ... 8 threads for the first parallel application, then ... use a cyclic distribution to cluster the accesses ...

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[Power-aware microarchitecture: design and modeling challenges for next-generation microprocessors - group of 16 »](#)

DM Brooks, P Bose, SE Schuster, H Jacobson, PN ... - Micro, IEEE, 2000 - ieeexplore.ieee.org

... analysis at the circuit and microarchitecture levels) of an adaptive issue queue ... monitored and fed back to control the progress of later instruction processing ...

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[Instruction-level distributed processing - group of 13 »](#)

JE Smith - Computer, 2001 - Springer

... are used, especially if multiple parallel copies of ... the processor is shared as in SMT, but any ... 21264 microarchitecture there are two clusters, with different ...

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SW Keckler, WJ Dally, D Maskit, NP Carter, A Chang ... - Proceedings of the 25th annual international symposium on ..., 1998 - portal.acm.org

... A synchronization pipeline stage holds instructions from each ... to consecutive addresses to proceed in parallel. ... Each cluster has its own 4KB instruction cache ...

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[An Empirical Study of Hyper-Threading in High Performance Computing Clusters - group of 5 »](#)

T Leng, R Ali, J Hsieh, V Mashayekhi, R ... - Linux HPC Revolution, 2002 - nubes.uspnet.usp.br

... Intel's Hyper-Threading implements SMT in such a way that each ... regardless of the number of nodes or CPUs of the cluster. ... NAS Parallel Benchmark - FT (Class B ...

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A Snively, DM Tullsen, G Voelker - ACM SIGMETRICS Performance Evaluation Review, 2002 - portal.acm.org

... level parallelism in the instruction issue queues. The multiple threads which will run on an SMT processor might come from parallel applications, multiple ...

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[... network-on-chip architecture for digital signal processing based on simultaneous multithreading \(SMT ...](#)

M Berekovic, P Pirsch - 5th Workshop on Media and Streaming Processor - pdcl.eng.wayne.edu

... 32], significantly improves performance on parallel workloads without ... decoupled (Figure 2). SMT processors with ... for communication between the clusters and thus ...

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